

JStik and JSimm Pin Numbering and Signal Description (also see important notes which follow)

JStik/ JSimm # <small>(note 5)</small>	Name	I/O <small>(note 6)</small>	Description
1	IOC0 SCS0(L)	I/O	8 mA sink/source I/O pin, or SPI Slave Chip Select 0 or slave mode select (when JStik is an SPI slave).
2	IOC1 SCS1(L)	I/O	8 mA sink/source I/O pin, or SPI Slave Chip Select 1
3	IOC3 SCS3(L)	I/O	8 mA sink/source I/O pin, or SPI Slave Chip Select 3
4	not used	-	Not used by JStik. This is the SimmStick and JSimm VRAW input 7.5V-18 VDC
5	3.3 VDC	PWR I/O	JStik's power converter provides output of 3.3V @ 100 mA on this pin for your use, when you power JStik's 5-14 VDC input (pin7). Or you can drive this pin with regulated 3.3VDC +/- 5%. (note 7). Jumper JP4 must be closed to enable 3.3V input or output on this pin. This pin is SimmStick bus ClkIn/A4, is not used on many SimmStick products, and SimmStick does not have a 3.3V pin, so we chose it for 3.3V on the JSimm bus and it will typically have no conflict on SimmStick systems.
6	CLK0	0	Clkout signal, a programmable divider output. Enabled by jumper JP1. Buffered from the aJ-100.
7	5-14 VDC	PWR I	5-14 VDC +/- 10% can be used as JStik power input. This is the normal power input.
8	CRST(L)	I/O	Open-drain reset to/from JStik. Use this signal to reset JStik from external logic or to reset your external logic when JStik resets. There is no reset pushbutton on JStik.
9	GND	PWR	GND (note 3)
10	IOB4	I/O	8 mA sink/source I/O pin. Can be CS4 but not used as such on JStik.
11	IOB5	I/O	8 mA sink/source I/O pin. Can be CS5 but not used as such on JStik.
12	IOD5 RXDA	I/O	8 mA sink/source I/O pin, or UARTA RXD (On the JSimm bus this signal is TTL level- not RS232 level - note 8)
13	IOD4 TXDA	I/O	8 mA sink/source I/O pin, or UARTA TXD (On the JSimm bus this signal is TTL- not RS232 level - note 8)
14	IOC2 SCS2(L)	I/O	8 mA sink/source I/O pin, or SPI Slave Chip Select 2
15	IOA0 IOE0	I/O	IOA0: 24 mA sink/source I/O pin. IOE0: 8 mA sink/source I/O pin, or Timer/Counter 2 input control/output B.
16	IOA1 IOE2	I/O	IOA1: 24 mA sink/source I/O pin. IOE2: 8 mA sink/source I/O pin, or Timer/Counter 2 external clock input
17	IOA2 IOE5	I/O	IOA2: 24 mA sink/source I/O pin IOE5: 8 mA sink/source I/O pin, or Timer/Counter 1 external clock input
18	IOA3 TCLK0	I/O	IOA3: 24 mA sink/source I/O pin TCLK0: optional external clock input for Timer/Counter 0 or the aJ100 prescaler block
19	IOA4 IOE1	I/O	IOA4: 24 mA sink/source I/O pin IOE1: 8 mA sink/source I/O pin, or Timer/Counter 2 input control/output A
20	IOC4 SPI MOSI	I/O	8 mA sink/source I/O pin. Also functions as SPI MOSI when in SPI master mode and MISO when a SPI slave.
21	IOC5 SPI MISO	I/O	8 mA sink/source I/O pin. Also functions as SPI MISO when in SPI master mode and MOSI when a SPI slave.
22	IOC6 SPI CLK	I/O	8 mA sink/source I/O pin. Also SPI Transfer Clock.
23	IOA5 IOE3	I/O	IOA5: 24 mA sink/source I/O pin and GPIOE 8 mA sink/source I/O pin IOE3: 8 mA sink/source I/O pin or Timer/Counter 1 Input Control/Output B
24	IOA6 IOE4	I/O	IOA6: 24 mA sink/source I/O pin and GPIOE 8 mA sink/source I/O pin IOE4: 8 mA sink/source I/O pin or Timer/Counter 1 Input Control/Output A
25	IOA7 IOE6	I/O	IOA7: 24 mA sink/source I/O pin and GPIOE 8 mA sink/source I/O pin IOE6: 8 mA sink/source I/O pin or Timer/Counter 0 Input Control/Output B
26	IOE7 IOC7	I/O	IOE7: 8 mA sink/source I/O pin or Timer/Counter 0 Input Control/Output A IOC7: 8 mA sink/source I/O pin or UART Receive Clock
27	IOD0 TXDB	I/O	8 mA sink/source I/O pin, or UARTB TXD (TTL- not RS232 level - note 8)
28	IOD1 RXDB	I/O	8 mA sink/source I/O pin, or UARTB RXD (TTL- not RS232 level - note 8)
29	IOD6 CTSA	I/O	8 mA sink/source I/O pin, also can function as CTS for serial A if JP5 is closed
30	N/C	-	Do not connect to this pin. May be used in a future version.